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Fig. 1 A

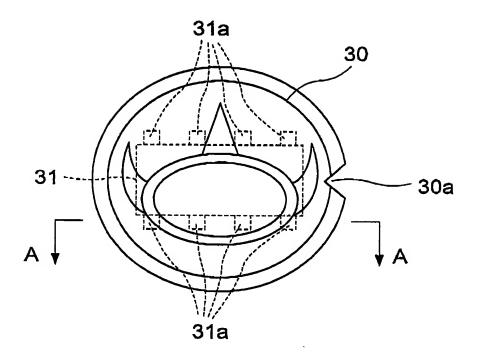
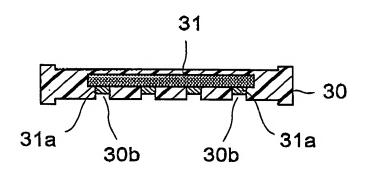


Fig. 1 B



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Fig. 1 C



Fig. 1 D

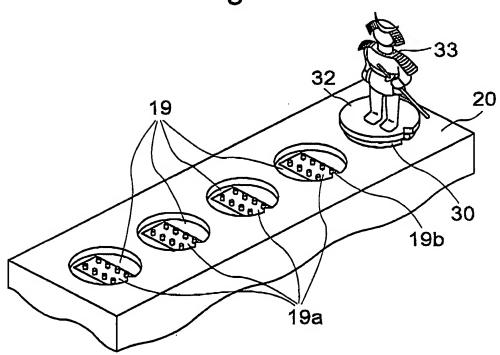
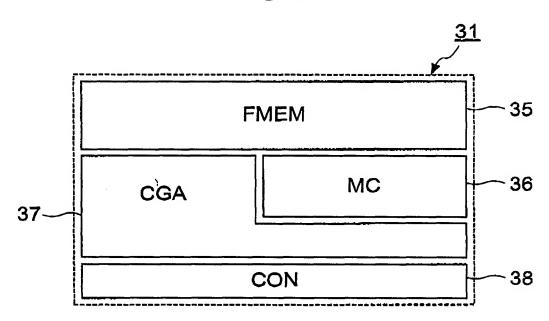
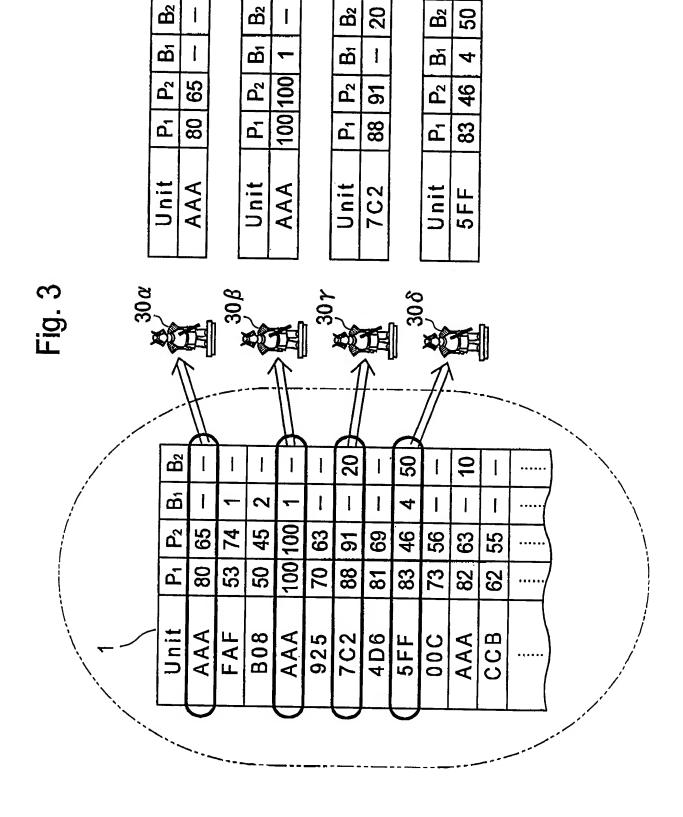
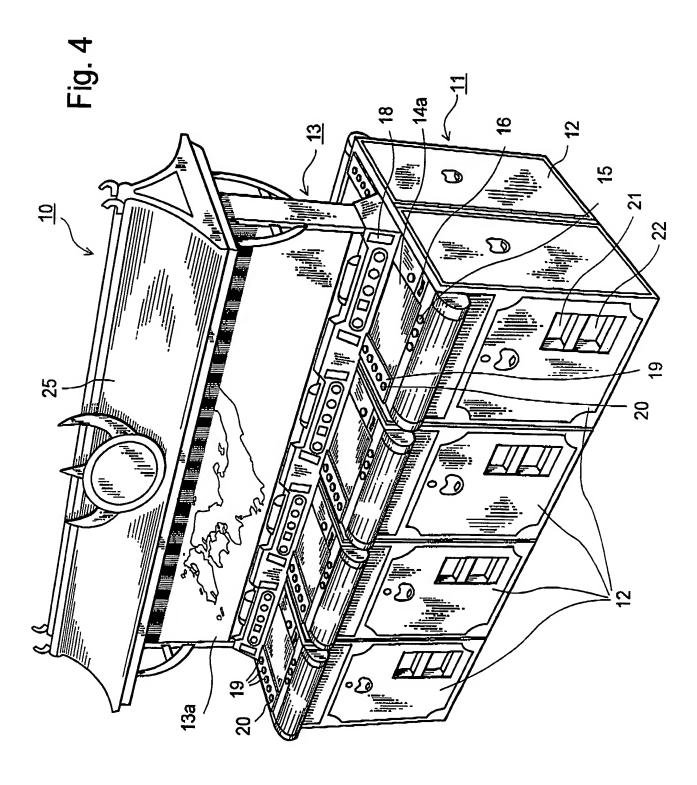


Fig. 2







6/8 Fig. 5 A 100 101 102 12 ROM Satellite 103 RAM Satellite 12 CPU -12 104 Satellite Flash memory 105 **VDP** arge display device 13 106 Frame buffer

Fig. 5 B <u>12</u> 131 130 132 15 Control unit ROM 120 **133** RAM Medal sensor 121 **↓138** Hopper Flash memory 122 134 SPU Medal 30 detection unit 35 112 **135** Sound buffer CPU Information reading/writing device Built-in IC **136** chip medal VDP **↓137** Information Frame buffer 113 writing device lmage display device Built-in-IC-chip 114 14 medal payout device 18 Speaker

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Fig. 6 A

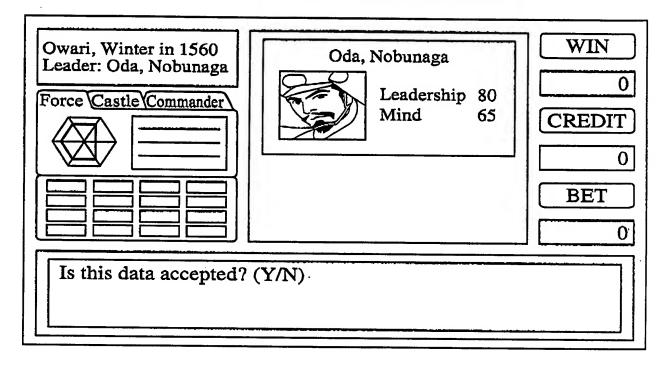
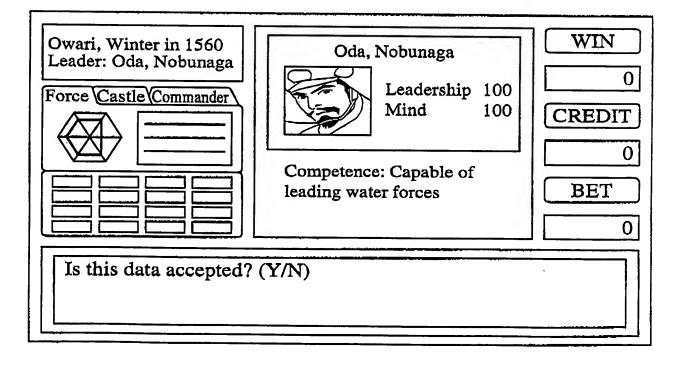


Fig. 6 B



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Fig. 6 C

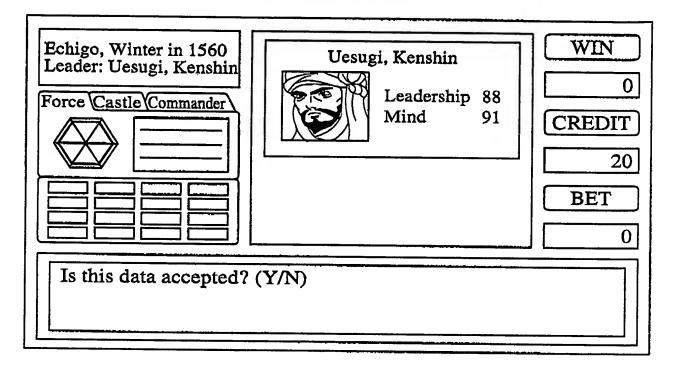


Fig. 6 D

